

Appl. No. 10/099,687
 Amendment and/or Response
 Reply to Office action of 13 March 2006

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Amendments to the Claims:

A listing of the entire set of pending claims (including amendments to the claims, if any) is submitted herewith per 37 CFR 1.121. This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1-8 (Canceled)

9. (New) A method comprising:

communicating a reference indicator from a first functional module to a second functional module, the reference indicator being indicative of a location of a third functional module in a memory, and

executing the third functional module from the second functional module based on the reference indicator.

10. (New) The method of claim 9, including:

relocating the third functional module to an other location in the memory, updating the reference indicator to be indicative of the other location, communicating the updated reference indicator from the first functional module to the second, and

executing the third functional module from the second functional module based on the updated reference indicator.

11. (New) The method of claim 10, wherein

relocating the third functional module to the other location includes modifying the third functional module.

12. (New) The method of claim 10, wherein

relocating the third functional module to the other location includes replacing the third functional module.

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13. (New) The method of claim 9, wherein
executing the third functional module is avoided if the reference indication
includes a null indicator.

14. (New) A system comprising:

a processor,

a memory that is configured to store a plurality of functional modules, each
functional module having an associated location in the memory and containing
code that controls the processor when the code is executed by the processor,

wherein

the plurality of functional modules includes at least a first, second, and
third module,

the first module is configured to cause the processor to communicate a
reference indicator to the second module, the reference indicator being indicative
of the location of the third module, and

the second module is configured to cause the processor to execute the
third module based on the reference indicator.

15. (New) The system of claim 14, wherein

the first module is configured to communicate an updated reference
indicator to the second module if the location of the third module in the memory is
changed, and

the second module is configured to cause the processor to execute the
third module based on the updated reference indicator.

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16. (New) The system of claim 14, wherein

the first module is configured to communicate a null reference indicator to the second module if the third module is removed from the memory, and
the second module is configured to cause the processor to avoid execution of the third module based on the null reference indicator.

17. (New) A computer program that, when executed by a processor, causes the processor to:

determine a plurality of reference indicators corresponding to locations of a plurality of functional modules, each module of the plurality of functional modules being executable by the processor,

upon commencement of execution of each module, communicate one or more of the reference indicators to the module, and

execute at least one other module based on the one or more reference indicators that were communicated to the module.

18. (New) The computer program of claim 17, that causes the processor to:

identify one or more absent modules, and

associate a null-value to each reference indicator corresponding to the one or more absent modules;

wherein

each module is configured to prevent the processor from executing any module having a null-value reference indicator.